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NOTICE OF ALLOWANCE AND FEE(S) DUE

35525

7590

12/29/2010

IBM CORP (YA) C/O YEE & ASSOCIATES PC P.O. BOX 802333 DALLAS, TX 75380 EXAMINER

VU, TUAN A

ART UNIT PAPER NUMBER

2193

DATE MAILED: 12/29/2010

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.

10/675,776 09/30/2003 Jimmie Earl DeWitt JR. AUS920030481US1 6262

TITLE OF INVENTION: METHOD AND APPARATUS FOR COUNTING EXECUTION OF SPECIFIC INSTRUCTIONS AND ACCESSES TO SPECIFIC DATA LOCATIONS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1510	\$300	\$0	\$1810	03/29/2011

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

HOW TO REPLY TO THIS NOTICE:

I. Review the SMALL ENTITY status shown above.

If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

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B. If the status above is to be removed, check box 5b on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and twice the amount of the ISSUE FEE shown above, or

If the SMALL ENTITY is shown as NO:

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B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check box 5a on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and 1/2 the ISSUE FEE shown above.

II. PART B - FEE(S) TRANSMITTAL, or its equivalent, must be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted. If an equivalent of Part B is filed, a request to reapply a previously paid issue fee must be clearly made, and delays in processing may occur due to the difficulty in recognizing the paper as an equivalent of Part B.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

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Complete and send this form, together with applicable fee(s), to: Mail Mail Stop ISSUE FEE

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or <u>Fax</u> (571)-273-2885

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DALLAS, TX 7	5380								(Depositor's name)
									(Signature)
									(Date)
APPLICATION NO.	FILING DATE		FIRST NAMED INVENT	OR		ATTO	RNEY DOCKET NO.	COI	NFIRMATION NO.
10/675,776	09/30/2003	•	Jimmie Earl DeWitt J	R.		ΑU	S920030481US1		6262
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APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DU	Æ	PREV. PAID ISSUI	E FEE	TOTAL FEE(S) DUE		DATE DUE
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PLEASE NOTE: Unl recordation as set fort (A) NAME OF ASSIG	less an assignee is ident h in 37 CFR 3.11. Com GNEE	A TO BE PRINTED ON tified below, no assignee pletion of this form is NC	data will appear on the T a substitute for filing (B) RESIDENCE: (CI	e pat an a	tent. If an assign ssignment. and STATE OR C	OUNT	RY)		
Please check the appropr	iate assignee category or	r categories (will not be p	rinted on the patent):		Individual 🖵 Co	rporati	on or other private gro	oup en	tity 🗖 Government
4a. The following fee(s) are submitted: ☐ Issue Fee ☐ Publication Fee (No small entity discount permitted) ☐ Advance Order - # of Copies			4b. Payment of Fee(s): (Please first reapply any previously paid issue fee shown above) ☐ A check is enclosed. ☐ Payment by credit card. Form PTO-2038 is attached. ☐ The Director is hereby authorized to charge the required fee(s), any deficiency, or credit any overpayment, to Deposit Account Number (enclose an extra copy of this form).						
5. Change in Entity Sta	,	· · · · · · · · · · · · · · · · · · ·			1			ED 1.6	27()(2)
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Authorized Signature					Date				
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PTOL-85 (Rev. 08/07) Approved for use through 08/31/2010.

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C/O YEE & ASSC		ART UNIT	PAPER NUMBER			
P.O. BOX 802333 DALLAS, TX 753		2193				
			DATE MAILED: 12/29/2010			

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 767 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 767 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 or (571)-272-4200.

	Application No.	Applicant(s)	
	10/675,776	DEWITT ET AL.	
Notice of Allowability	Examiner	Art Unit	
	TUAN A. VU	2193	
	TOAN A. VO	2193	
The MAILING DATE of this communication appeal all claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIOF of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED ir or other appropriate commulGHTS. This application is s	this application. If not included unication will be mailed in due cours	
1. \square This communication is responsive to $\underline{12/01/10}$.			
2. X The allowed claim(s) is/are <u>1, 6, 26, 29-30, 32, 34-35, 38-3</u>	<u>89 (renum 1-10)</u> .		
 3. Acknowledgment is made of a claim for foreign priority ur a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 		or (f).	
2. ☐ Certified copies of the priority documents have		n No	
Copies of the certified copies of the priority does	• •		om the
International Bureau (PCT Rule 17.2(a)).	cuments have been received	an the national stage application in	Jili tilo
* Certified copies not received:			
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		a reply complying with the requiren	nents
 A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give 			E OF
5. CORRECTED DRAWINGS (as "replacement sheets") mus	st be submitted.		
(a) \square including changes required by the Notice of Draftspers	on's Patent Drawing Review	v (PTO-948) attached	
1) 🗌 hereto or 2) 🔲 to Paper No./Mail Date	•		
(b) ☐ including changes required by the attached Examiner's Paper No./Mail Date	s Amendment / Comment or	in the Office action of	
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t			of
6. DEPOSIT OF and/or INFORMATION about the depo- attached Examiner's comment regarding REQUIREMENT			ne
Attachment(s) 1. ☐ Notice of References Cited (PTO-892)	5. Notice of In	formal Patent Application	
 Notice of Preferences Gled (P10-692) Dotice of Draftperson's Patent Drawing Review (PTO-948) 		ummary (PTO-413),	
3. ☑ Information Disclosure Statements (PTO/SB/08),	Paper No.	Mail Date Amendment/Comment	
Paper No./Mail Date 7/19/10			
 Examiner's Comment Regarding Requirement for Deposit of Biological Material 		Statement of Reasons for Allowance	е
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/Tuan A Vu/ Examiner, Art Unit 2193			

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DETAILED ACTION

1. This action is responsive to the Applicant's response filed 12/01/10 (Appeal Brief).

As indicated in Applicant's response, no claims have been amended. Claims 1, 6, 26, 29-30, 32, 34-35, 38-39 are pending in the office action.

EXAMINER'S STATEMENT OF REASONS FOR ALLOWANCE

2. Claims 1, 6, 26, 29-30, 32, 34-35, 38-39 are allowed.

The following is an examiner's statement of reasons for allowance.

3. The prior art taken separately or jointly does not suggest or teach the following features.

An "instruction cache unit" in terms of:

.... receiving a bundle at an instruction cache unit, the bundle containing at least one instruction slot, wherein the instruction slot contains an instruction;

responsive to receiving the bundle, determining by the instruction cache unit whether the bundle contains an indicator within at least one spare bit of the at least one instruction slot, wherein the indicator identifies the instruction as one that is to be monitored by a performance monitor unit;

responsive to a determination that the bundle contains the indicator within the at least one instruction slot, sending a signal by the instruction cache unit to a performance monitor unit,

wherein upon receiving the signal, the performance monitor unit increments a counter in the instruction cache unit that is associated with the instruction, the incrementing providing a count of a number of times the instruction is executed; and

sending the bundle from the instruction cache unit to a functional unit for execution of the instruction (e.g. in claim 1, or 32)

Art Unit: 2193

No prior art discloses 'instruction cache unit" as described by the invention, which has been pointed to by the Applicants in the course of the prosecution that this is a unit equipped with particular structures one of whose illustrative aspect is depicted in Figure 31 (see Applicants Response per 4/20/2009), as that was to overcome the Office insistence of the fact that instruction cache in general cannot be equipped with capability to analyze instruction and send command based thereon, as initially claimed. That is, the originally recited "instruction cache" had been adjusted to become 'instruction cache unit" based on the corrected Specifications that was entered (so it to be "consistent with the functionality already described" in the original disclosure) with said Applicants response in terms of:

Replacement Drawing Sheets and Annotated Drawings Sheets are attached hereto. Please amend FIG. 2, FIG. 3, FIG. 22, and FIG. 31 as indicated on the Replacement Drawing sheets attached hereto. In FIG. 2, instance 214 is amended to state "Instruction Cache Unit"; instance 216 is amended to state "Data Cache Unit." In FIG. 3, instance 300 is amended to state "Instruction Cache Unit." In FIG. 22, instance 2200 is amended to state "Instruction Cache Unit." In FIG. 31, instance 3106 is amended to state "Instruction Cache Unit."

That is, Applicant's expressed clarification for the USC 112 issue regarding the "instruction cache" (not depicted by the Disclosure) was to be followed by Applicant's proving that this instruction cache unit (as amended) has particular structures described in Figure 31, which would be different from the cache or cited portions in the rejection using Gover, via arguments reproduced hereafter (Applicant Arguments per 4/20/09, pg. 25 of 26).

By contrast, the claims feature an <u>instruction cache</u> unit that includes additional logic for processing data, e.g. counting, selecting, and transmitting data. One representation of the disclosed instruction cache unit recited in the claim is illustrated in Figure 31, reproduced infra.

In FIG. 31, instance 3106 of illustrates an instruction cache unit that includes a counter space 3110, and range registers 3108 that may function in conjunction with counter 3110. Accordingly, it must be noted that the instruction cache unit possessed components that enable a processing capability which exceeds the functionality of the common <u>instruction cache</u> disclosed in Glover. For at least this reason along, the cited art of Glover cannot read on the claims. The asserted Admitted prior art, i.e. pages 2-3 of the specification, adds nothing to cure the deficiencies in Glover. Nowhere in the background of the

disclosure is there any reference to an instruction cache unit that performs processing as disclosed in the claims.

In summary, Glover and the asserted admitted prior art do not collectively or individually teach or fairly suggest an instruction <u>cache unit</u>. Therefore, for at least this reason a conclusion of prima facie obviousness cannot be established. It is well-settled that to establish a prima facie case of obviousness, all the claim limitations must be taught or suggested by the prior art. In re Royka, 490 F.2d 981,985,180 USPQ 580 (CCPA 1974).

Based on the above, <u>no prior art</u> has been identified to teach or suggest 'instruction cache unit' as illustrated by Figure 31 and expressed by the Applicant's facts based reasoning.

- 4. Further, the **Terminal Disclaimers** filed 10/14/2010 have been approved and thereby overcome all the outstanding Double patenting rejections of record.
- 5. Per this allowance, the effect of Applicant's Appeal Brief would be deemed moot.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tuan A Vu whose telephone number is (571) 272-3735. The examiner can normally be reached on 8AM-4:30PM/Mon-Fri.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lewis Bullock can be reached on (571)272-3759.

The fax phone number for the organization where this application or proceeding is assigned is (571) 273-3735 (for non-official correspondence - please consult Examiner before using) or 571-273-8300 (for official correspondence) or redirected to customer service at 571-272-3609.

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Art Unit: 2193

Any inquiry of a general nature or relating to the status of this application should be

directed to the TC 2100 Group receptionist: 571-272-2100.

Information regarding the status of an application may be obtained from the Patent Application

Information Retrieval (PAIR) system. Status information for published applications may be

obtained from either Private PAIR or Public PAIR. Status information for unpublished

applications is available through Private PAIR only. For more information about the PAIR

system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR

system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

/Tuan A Vu/

Primary Examiner, Art Unit 2193

December 20, 2010